

Power-Efficient VLC Transmitter Able to Reproduce Multi-Carrier Modulation Schemes by Using the Output Voltage Ripple of the HB-LED Driver

Juan Rodríguez¹, Diego G. Lamar¹, Daniel G. Aller¹, Pablo F. Miaja², and Javier Sebastián¹.

Departamento de Ingeniería Eléctrica, Electrónica, de Computadores y Sistemas, Universidad de Oviedo, Gijón 33204, Spain (e-mail: rodriguezjmjuan@uniovi.es)

Power System Division, European Space Agency, Noordwijk 2201 AZ, The Netherlands (e-mail: pablo.fernandez@esa.int)

Abstract— Visible Light Communication (VLC) consists in using High-Brightness LEDs (HB-LEDs) not only for lighting, but also for transmitting information by changing the light intensity rapidly. One of the most important problems of VLC is that the HB-LED drivers that have been proposed for achieving the highest bit rates offer low power efficiency. The reason is that since these HB-LED drivers must be able to reproduce fast current waveforms, the use of Linear Power Amplifiers (LPAs) has been adopted, which damages the power efficiency of HB-LED lighting. To the author's knowledge, there is only one approach reported in the literature that is able to reproduce the most recommended modulation schemes by using a power efficient HB-LED driver. However, since it is based on the use of pulse-width modulated dc-dc power converters, the required switching frequency is between 3 and 6 times higher than the maximum frequency of the communication signal. As a result, the approach suffers from high switching losses when the highest bandwidths are addressed. A method for reproducing the most recommended modulation schemes by modulating both the amplitude and the phase of the output voltage ripple is presented in this work. The major benefits are the high efficiency, the simplicity and the lower switching frequency required for the implementation.

Keywords—Visible Light Communication (VLC), High-Brightness LED (HB-LED), Ripple Modulation (RM) Technique, Multi-Carrier Modulation (MCM).

I. INTRODUCTION

Nowadays, wireless communication is essential for the society, enabling a lot of new applications that have recently emerged based on wireless connectivity. As a result, the mobile data traffic has grown exponentially during last decade, which has been translated into a saturation of the Radio Frequency (RF) spectrum [1]. Visible Light Communication (VLC) is one of the advanced candidates for alleviating the congestion of the RF spectrum [2]. It consists in using High-Brightness LEDs (HB-LEDs) not only for lighting, but also for transmitting information by changing the light intensity rapidly. In this application, the light intensity $[s(t)]$ has a DC component (s_{DC}) that determines the lighting level and an AC component $[s_{AC}(t)]$ that represents the transmitted information:

$$s(t) = s_{DC} + s_{AC}(t). \quad (1)$$

It is important to note that for safety reasons, the frequency components of $s_{AC}(t)$ are high enough to be unappreciable to the human eye (i.e. above 100 Hz approximately [3]).

Depending on the kind of HB-LED, its bandwidth ranges between 3 and 20 MHz [4]. However, the bit rate depends not

only on the available bandwidth, but also on the modulation scheme used to transmit the information. There are three main modulation strategies for VLC: Pulse-Based Modulation (PBM) schemes, Single-Carrier Modulation (SCM) schemes and Multi-Carrier Modulation (MCM) schemes. PBM consists in transmitting the information using light pulses. PBM schemes can be reproduced easily, but they are inefficient from the communication perspective and, as a result, they are not recommended for addressing high bit rates. Both SCM and MCM schemes are better options for satisfying this target [5]. In SCM schemes, the information is transmitted by changing the amplitude and the phase of a sinusoidal waveform (see Fig. 1). In this case, the communication component of $s(t)$ [i.e., $s_{AC}(t)$] can be described as follows:

$$s_{SCM}(t) = A_{S-SCM}(t) \cdot \cos(2 \cdot \pi \cdot f_{SCM} \cdot t + \phi_{S-SCM}(t)), \quad (2)$$

where f_{SCM} , $A_{S-SCM}(t)$ and $\phi_{S-SCM}(t)$ are the frequency of the sinusoid, the amplitude modulation and the phase modulation, respectively. Moreover, MCM schemes are made up of N sinusoidal waveforms with different frequencies (f_{MCM-1} , f_{MCM-2} , ... f_{MCM-N}) that also change the amplitude and the phase over time (see Fig. 2). In this case, $s_{AC}(t)$ can be expressed as follows:

$$s_{MCM}(t) = \frac{1}{N} \sum_{k=1}^N s_{MCM-k}(t), \quad (3)$$

$$s_{MCM-k}(t) = A_{S-MCM-k}(t) \cdot \cos(2 \cdot \pi \cdot f_{MCM-k} \cdot t + \phi_{S-MCM-k}(t)). \quad (4)$$

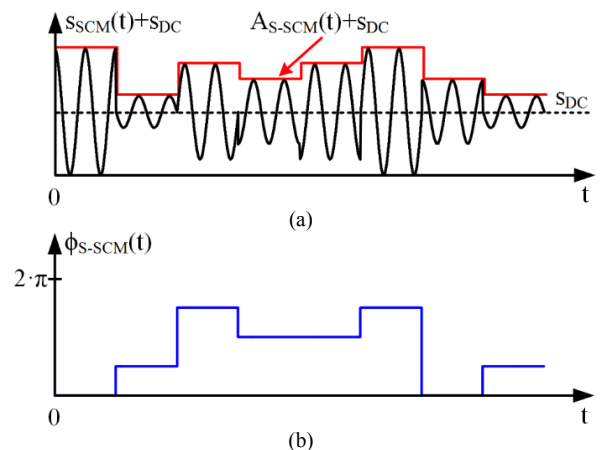


Fig. 1. Example of a SCM scheme for VLC: (a) SCM signal highlighting the amplitude. (b) Phase of the SCM signal.

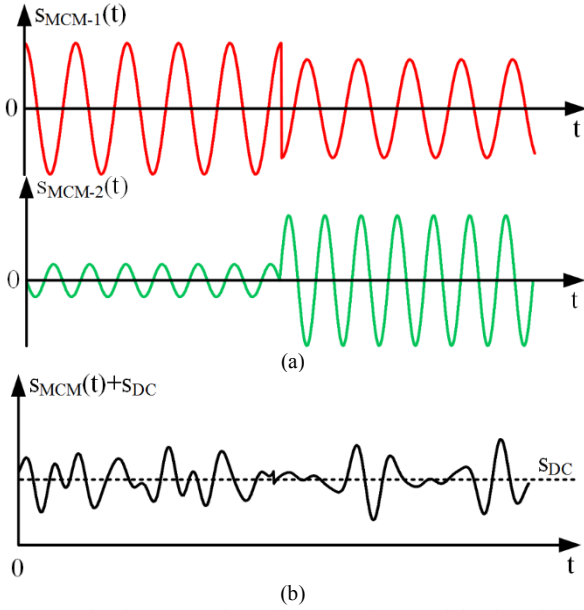


Fig. 2. Example of a MCM scheme for VLC: (a) Two of the sinusoids of the MCM schemes. (b) MCM signal.

Besides the bit rate, achieving low error during the demodulation process is essential for reaching a successful communication. In wireless communication, this error is strongly jeopardized by the Multipath Fading (MF). This phenomenon is caused by the different propagation paths of the signal, which is translated into different time delays of the light beams that reach the receiver, increasing the error during the demodulation process. Differently from SCM schemes, MCM schemes are robust against the MF problem and, as a consequence, they are the preferred modulation strategy for VLC [6]. The problem is that MCM schemes are reproduced by using Class A or AB Linear Power Amplifiers (LPAs), which offer a power efficiency that ranges between 10% and 40% [2]. Therefore, these LPAs jeopardize one of the main advantages of HB-LED lighting: the high power efficiency. This work aims to introduce a method for reproducing MCM schemes by using the output voltage ripple of a power converter, thus keeping high power efficiency. The paper is organized as follows. The operating principle of the HB-LED driver proposed for VLC is explained in Section II. The control system is described in Section III. The experimental results are given in Section IV and, finally, the conclusions are gathered in Section V.

II. REPRODUCING MCM SCHEMES BY USING THE OUTPUT VOLTAGE RIPPLE OF A POWER CONVERTER

A. HB-LED Drivers able to Reproduce MCM Schemes

Using a HB-LED driver fully based-on the use of power converters is a promising approach for avoiding the efficiency problem of LPAs [7]-[8]. However, to the author's knowledge, only a HB-LED driver for VLC able to reproduce MCM schemes can be found in the literature. The HB-LED driver presented in [9] is based on the use of pulse-width modulated dc-dc power converters, and is able to reproduce both SCM and MCM schemes. As Fig. 3(a) shows, the drawback of this approach is that the Pulse-Width Modulation (PWM) technique requires a switching frequency (f_{SW-PWM}) around 20 times higher than the maximum frequency of the communication signal (f_{S-Max}) [10]. As a result, switching losses are unaffordable when the highest bandwidths are addressed and both the devices of the power stage and the digital system responsible for the gate signals generation are

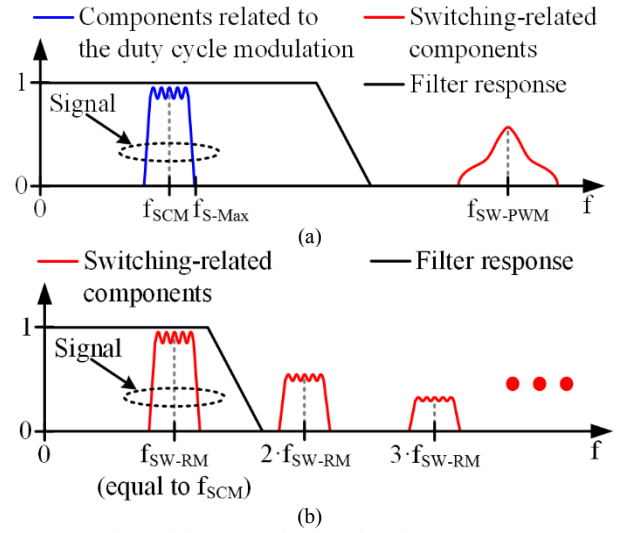


Fig. 3. Comparison of the approaches based on the use of power converters in the frequency domain: (a) PWM technique. (b) RM technique.

not conventional. According to [8]-[9], f_{SW-PWM} can be reduced to around 3 or 6 times f_{S-Max} by using sophisticated buck-derived converters.

A technique that enables the use of a lower switching frequency is reported in [11]. This technique will be noted as Ripple Modulation (RM) and it consists in modulating both the amplitude and the phase of the first switching harmonic of the output voltage ripple in order to reproduce an SCM signal. It is important to note that in the case of the RM technique, the switching frequency (f_{SW-RM}) is equal to the frequency of the SCM sinusoid (f_{SCM}). As a result, f_{SW-RM} is much lower than f_{SW-PWM} (see Fig. 3), which is translated into a high reduction of the switching losses and a less challenging implementation. For instance, if f_{SCM} is 10 MHz and f_{S-Max} is 13 MHz, f_{SW-PWM} would be around 60 MHz whereas f_{SW-RM} would be 10 MHz. The main drawback of the RM technique is that it has not been reported how to reproduce MCM schemes. This work aims to address this point and, moreover, to clearly describe the control system of the power converter.

B. Operating Principle of the RM Technique

As was previously mentioned, the RM technique enables the reproduction of SCM signals by modulating both the amplitude and the phase of the first switching harmonic of the output voltage ripple [11]. The HB-LED driver topology proposed for applying this technique is a two-phase synchronous buck converter with M^{th} order output filter (see Fig. 4). Differently from a conventional buck converter, the output filter passes not only the DC component, but also the first-switching harmonic of the pulse voltage waveforms of the switch-nodes [$v_{S-A}(t)$ and $v_{S-B}(t)$]. Therefore, and as Fig. 3(b) shows, the cut-off frequency of the filter (f_C) must be higher than the first switching harmonic and lower than the second one (i.e., $f_{SW-RM} < f_C < 2 \cdot f_{SW-RM}$). In this way, the output voltage [$v_O(t)$] is made up of a DC component (v_{O-DC}) and a sinusoid [$v_{O-AC}(t)$] whose frequency is f_{SW-R} :

$$v_O(t) = v_{O-DC} + v_{O-AC}(t), \quad (5)$$

$$v_{O-DC} = d \cdot V_G, \quad (6)$$

$$v_{O-AC}(t) = A_V(t) \cdot \cos(2 \cdot \pi \cdot f_{SW-RM} \cdot t + \phi_V(t)), \quad (7)$$

where d is the duty cycle, V_G is the input voltage, $A_V(t)$ is the amplitude of the sinusoid and $\phi_V(t)$ is the phase of the sinusoid.

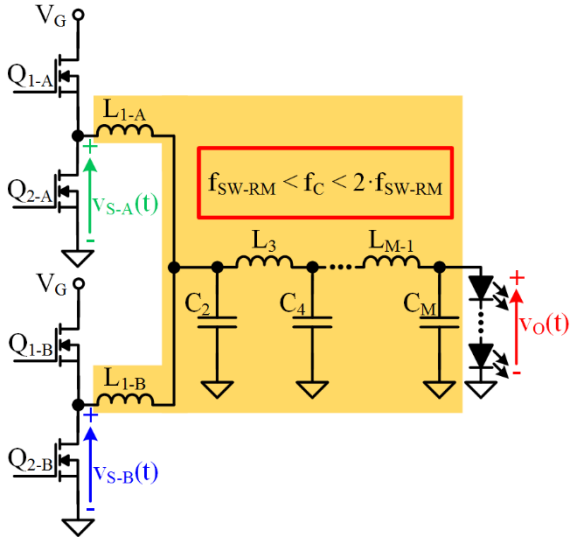


Fig. 4. Two-phase synchronous buck converter with no conventional filter for applying the RM technique.

Differently from the PWM technique, the RM technique is based on controlling not only the width of the voltage pulses at the input of the filter [i.e., the duty cycle of $v_{S-A}(t)$ and $v_{S-B}(t)$], but also the position over the switching period (see Fig. 5). In particular, the width modulation is the same for both phases, but the position over the switching period is controlled individually for each one. As will be demonstrated later, this strategy enables the control of both $A_v(t)$ and $\phi_v(t)$. It is important to note that controlling the position of the pulses is equivalent to control their phases. The dimensionless parameter that is used to control the phase of a pulse will be noted as γ [see Fig. 5 (b)], and it ranges between 0 and 1. When γ is 0, the phase is 0° , and when γ is 1, the phase is -2π . Therefore, the phase of a pulse is equal to $-\gamma \cdot 2\pi$ and, as a consequence, the center of the pulse appears $\gamma \cdot T_{SW}$ after the beginning of the switching period [see Fig. 5 (b)]. Note that the phase is determined by the center of the pulse.

The equivalent circuits depicted in Fig. 6 will support the explanation of how $A_v(t)$ and $\phi_v(t)$ can be modulated by controlling the phase (i.e., the position) of $v_{S-A}(t)$ and $v_{S-B}(t)$. Basically, $v_{S-A}(t)$ and $v_{S-B}(t)$ are pulse voltage waveforms determined by the MOSFETs [Q_{1-A} and Q_{2-A} in the case of $v_{S-A}(t)$, and Q_{1-B} and Q_{2-B} in the case of $v_{S-B}(t)$]. Therefore, these voltages can be considered as ideal pulse voltage sources [see Fig. 6(a)]. The superposition theorem allows us to calculate $v_o(t)$ by analyzing the voltage contribution of each phase [see Fig. 6(b)]:

$$v_o(t) = v_{O-A}(t) + v_{O-B}(t), \quad (8)$$

where $v_{O-A}(t)$ and $v_{O-B}(t)$ are the voltages provided by phases A and B, respectively. The circuit depicted in Fig. 6(c) can be obtained by considering that the phase inductors are equal and by applying the Thevenin's theorem. This circuit considers $v_{S-A}(t)$ divided by two and applied to a low-pass filter, where the equivalent first inductor (L_1) is half the phase inductor. Taking into account that the filter passes not only the DC component, but also the first switching harmonic; the voltage contribution of each phase can be calculated as follows:

$$v_{O-A}(t) = v_{O-A-DC} + v_{O-A-AC}(t), \quad (9)$$

$$v_{O-A-DC} = \frac{d \cdot V_G}{2}, \quad (10)$$

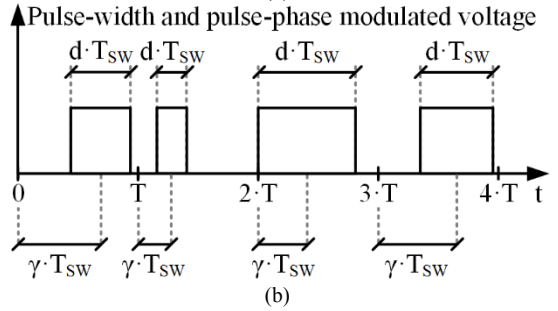
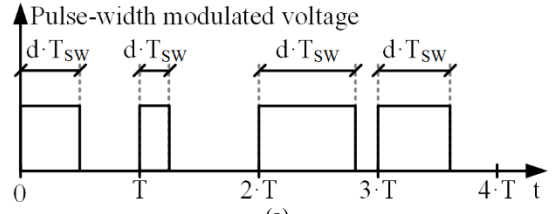


Fig. 5. Two strategies for controlling a power converter: (a) Pulse-width modulation control. (b) Pulse-width and pulse-phase modulation control.

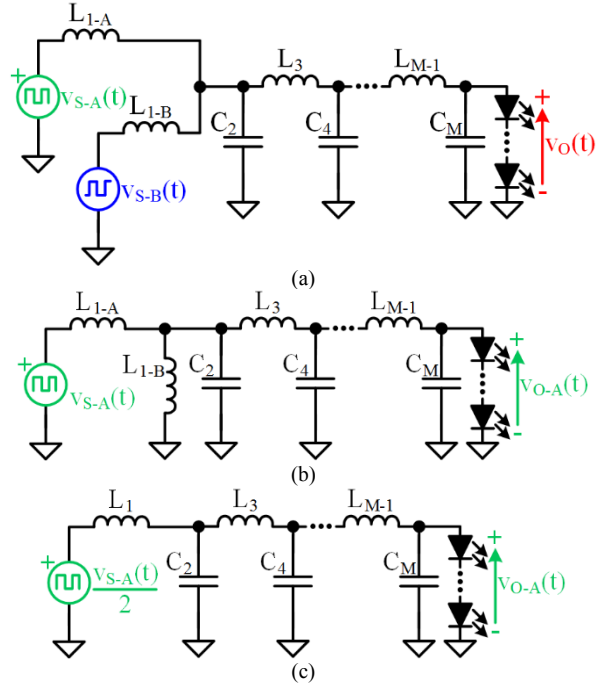


Fig. 6. Equivalent circuits of the two-phase synchronous buck converter for applying the RM technique: (a) Considering the switch-node voltages as ideal pulse voltage sources. (b) Applying the superposition theorem (example with phase A). (c) Applying the Thevenin's theorem (example with phase A).

$$v_{O-A-AC}(t) = \frac{V_G}{\pi} \sin(d \cdot \pi) \cdot \cos(2 \cdot \pi \cdot f_{SW-RM} \cdot t - \gamma_A(t) \cdot 2 \cdot \pi), \quad (11)$$

$$v_{O-B}(t) = v_{O-B-DC} + v_{O-B-AC}(t), \quad (12)$$

$$v_{O-B-DC} = \frac{d \cdot V_G}{2}, \quad (13)$$

$$v_{O-B-AC}(t) = \frac{V_G}{\pi} \sin(d \cdot \pi) \cdot \cos(2 \cdot \pi \cdot f_{SW-RM} \cdot t - \gamma_B(t) \cdot 2 \cdot \pi), \quad (14)$$

where $\gamma_A(t)$ and $\gamma_B(t)$ are the dimensionless control parameters that determine the phase of the voltage pulses (i.e., the position of the pulses over the switching period) [see Fig. 7(a)-(b)]. Substituting (9)-(14) into (8) allows us to obtain $v_o(t)$:

$$v_{O-DC} = v_{O-A-DC} + v_{O-B-DC} = d \cdot V_G, \quad (15)$$

$$v_{O-AC}(t) = v_{O-A-AC}(t) + v_{O-B-AC}(t) = A_V[\gamma_1(t), \gamma_2(t), d] \cdot \cos(2 \cdot \pi \cdot f_{SW-RM} \cdot t + \phi_V[\gamma_1(t), \gamma_2(t)]), \quad (16)$$

$$A_V[\gamma_1(t), \gamma_2(t), d] = \frac{2 \cdot V_G}{\pi} \sin(d \cdot \pi) \cos[(\gamma_2(t) - \gamma_1(t)) \cdot \pi], \quad (17)$$

$$\phi_V[\gamma_1(t), \gamma_2(t)] = \frac{\gamma_1(t) + \gamma_2(t)}{2} \cdot 2 \cdot \pi. \quad (18)$$

Therefore, the phases of the pulses determine both $A_V(t)$ and $\phi_V(t)$. In particular, $A_V(t)$ depends on the phase-shift between the pulses, and $\phi_V(t)$ depends on the average phase of the pulses [see Fig. 7(c)]. Hence, it is recommendable to focus on the phase-shift and on the average phase instead of on the individual phases. In this way, two new parameters are introduced in order to replace $\gamma_1(t)$ and $\gamma_2(t)$. First, $\alpha(t)$ is a dimensionless parameter that determines the phase-shift between the pulses (i.e., the position difference) and, as a consequence, $A_V(t)$. Second, $\beta(t)$ determines the average phase of the pulses (i.e., the average position) and, as a result, $\phi_V(t)$. These two parameters can be defined as follows:

$$\alpha(t) = \gamma_2(t) - \gamma_1(t), \quad (19)$$

$$\beta(t) = \frac{\gamma_1(t) + \gamma_2(t)}{2}. \quad (20)$$

It is considered that $\gamma_1(t)$ is always lower than $\gamma_2(t)$ and that the difference never overcomes 0.5. This ensures that $A_V(t)$ is always higher than 0.

Substituting (19) and (20) into (17) and (18) leads to the following equations:

$$A_V[\alpha(t), d] = \frac{2 \cdot V_G}{\pi} \sin(d \cdot \pi) \cos[\alpha(t) \cdot \pi], \quad (21)$$

$$\phi_V[\beta(t)] = \beta(t) \cdot 2 \cdot \pi. \quad (22)$$

In summary, the RM technique allows us to provide an output voltage that is made up of a DC component and a sinusoid whose frequency is equal to the switching frequency (i.e., f_{SW-RM}). The duty cycle (i.e., d) determines v_{O-DC} . The amplitude of the sinusoid [i.e., $A_V(t)$] and its phase [i.e., $\phi_V(t)$] can be controlled with the phase-shift between the pulses [i.e., by using $\alpha(t)$] and with the average phase of the pulse [i.e., by using $\beta(t)$], respectively. In order to reproduce a particular waveform, the first step is to determine d to obtain the desired DC component:

$$d = \frac{v_{O-DC}}{V_G}. \quad (23)$$

The second step is to calculate $\alpha(t)$ in order to obtain the desired amplitude of the output voltage sinusoid:

$$\alpha(t) = \frac{1}{\pi} \cos^{-1} \left(\frac{\pi \cdot A_V(t)}{2 \cdot V_G \cdot \sin(d \cdot \pi)} \right). \quad (24)$$

Finally, the third step is to calculate $\beta(t)$ in order to obtain the desired phase of the output voltage:

$$\beta(t) = \frac{-\phi_V(t)}{2 \cdot \pi}. \quad (25)$$

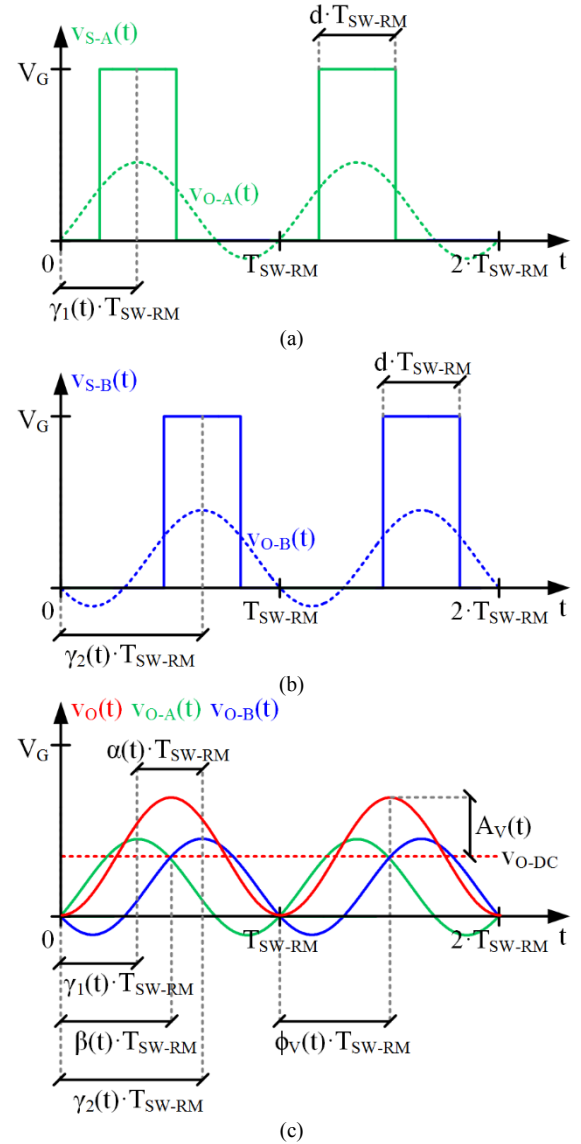


Fig. 7. Main voltage waveforms of the two-phase synchronous buck converter used to apply the RM technique: (a) Switch-node voltage of phase A and output voltage component that it provides. (b) Switch-node voltage of phase B and output voltage component that it provides. (c) Graphical calculation of the output voltage from the components provided by each phase.

C. Using the RM Technique to Reproduce MCM Schemes

As previously explained, the RM technique has an important drawback: it was proposed for reproducing SCM schemes and it has not been reported how to reproduce MCM schemes (i.e., the most recommended modulation schemes for VLC). Reproducing SCM schemes using the RM technique is straightforward by comparing equation (2) (i.e., the desired light intensity) and equations (16)-(18) (i.e., the AC voltage obtained with the RM technique) and by considering the linear relationship between $s(t)$ and $v_O(t)$ when the HB-LEDs are properly biased:

$$A_V(t) = K_{V-S} \cdot A_{S-SCM}(t), \quad (26)$$

$$\phi_V(t) = \phi_{S-SCM}(t), \quad (27)$$

where K_{V-S} is the proportionality coefficient between $v_{O-AC}(t)$ and $s_{O-AC}(t)$. Hence, f_{SW-RM} must be equal to f_{SCM} , $A_V(t)$ must track a reference that is proportional to the amplitude modulation and $\phi_V(t)$ must be equal to the phase modulation. Substituting (26) and (27) into (24) and (25) allows us to

obtain how $\alpha(t)$ and $\beta(t)$ must be controlled in order to reproduce SCM schemes:

$$\alpha(t) = \frac{1}{\pi} \cos^{-1} \left(\frac{\pi \cdot K_{V-S} \cdot A_{S-SCM}(t)}{2 \cdot V_G \cdot \sin(d \cdot \pi)} \right), \quad (28)$$

$$\beta(t) = \frac{-\phi_{S-SCM}(t)}{2 \cdot \pi}. \quad (29)$$

The analysis for MCM schemes is not as trivial as in the case of SCM schemes. Since MCM schemes are made up of several sinusoids with different frequencies [check equations (3)-(4)], identifying the references that $A_V(t)$ and $\phi_V(t)$ must track is not straightforward. In order to obtain those references, equations (3)-(4) must be manipulated to reach an expression where only a single sinusoid changes the amplitude and the phase over time. The first step is to calculate the center frequency of the scheme as follows:

$$f_{MCM} = \frac{1}{N} \sum_{k=1}^N f_{MCM-k}. \quad (30)$$

After that, the difference between f_{MCM} and the frequency of each sinusoid can be expressed as follows (see Fig. 8):

$$\Delta f_{MCM-k} = f_{MCM-k} - f_{MCM}. \quad (31)$$

Substituting (31) into (4) leads to:

$$\begin{aligned} s_{MCM-k}(t) &= A_{S-MCM-k}(t) \cdot \\ &\cdot \cos(2 \cdot \pi \cdot (f_{MCM} + \Delta f_{MCM-k}) \cdot t + \phi_{S-MCM-k}(t)) \\ &= A_{S-MCM-k}(t) \cdot \cos(2 \cdot \pi \cdot f_{MCM} \cdot t) \cdot \\ &\cdot \cos(2 \cdot \pi \cdot \Delta f_{MCM-k} \cdot t + \phi_{S-MCM-k}(t)) - \\ &- A_{S-MCM-k}(t) \cdot \sin(2 \cdot \pi \cdot f_{MCM} \cdot t) \cdot \\ &\cdot \sin(2 \cdot \pi \cdot \Delta f_{MCM-k} \cdot t + \phi_{S-MCM-k}(t)). \end{aligned} \quad (32)$$

Substituting (32) into (3) leads to:

$$s_{MCM}(t) = s_{MCM-I}(t) \cdot \cos(2 \cdot \pi \cdot f_{MCM} \cdot t) - s_{MCM-Q}(t) \cdot \sin(2 \cdot \pi \cdot f_{MCM} \cdot t), \quad (33)$$

where $s_{MCM-I}(t)$ and $s_{MCM-Q}(t)$ are the in-phase and quadrature component of $s_{MCM}(t)$ with respect to a sinusoid whose frequency is f_{MCM} . These two components are formulated as:

$$s_{MCM-I}(t) = \frac{1}{N} \sum_{k=1}^N A_{S-MCM-k}(t) \cdot \cos(2 \cdot \pi \cdot \Delta f_{MCM-k} \cdot t + \phi_{S-MCM-k}(t)), \quad (34)$$

$$s_{MCM-Q}(t) = \frac{1}{N} \sum_{k=1}^N A_{S-MCM-k}(t) \cdot \sin(2 \cdot \pi \cdot \Delta f_{MCM-k} \cdot t + \phi_{S-MCM-k}(t)). \quad (35)$$

Finally, the MCM signal can be expressed as a function of a single sinusoid that changes the amplitude and the phase over time:

$$s_{MCM}(t) = A_{S-MCM}(t) \cdot \cos(2 \cdot \pi \cdot f_{MCM} t + \phi_{S-MCM}(t)), \quad (36)$$

$$A_{S-MCM}(t) = \sqrt{s_{MCM-I}(t)^2 + s_{MCM-Q}(t)^2}, \quad (37)$$

$$\phi_{S-MCM}(t) = \tan^{-1} \left(\frac{s_{MCM-Q}(t)}{s_{MCM-I}(t)} \right). \quad (38)$$

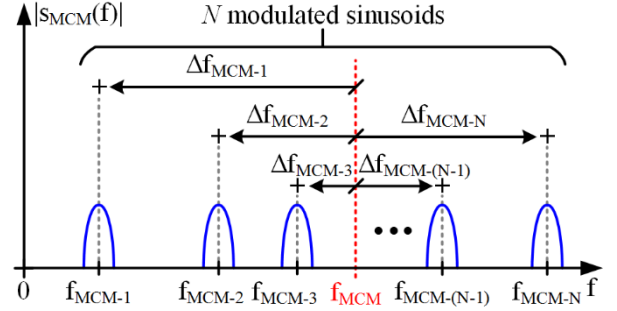


Fig. 8. MCM scheme in the frequency domain.

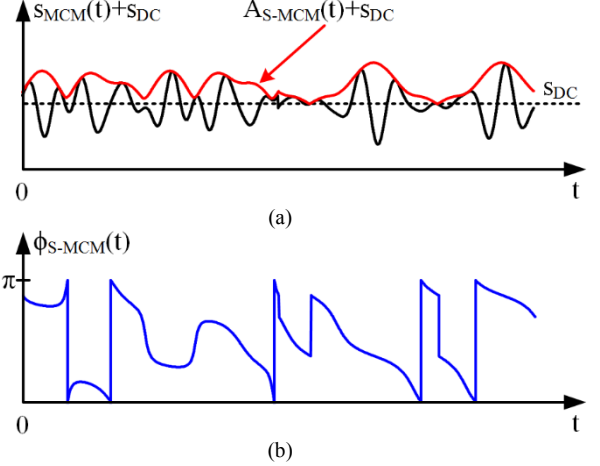


Fig. 9. Envelope (a) and instantaneous phase (b) of a MCM signal.

It is important to note that $A_{S-MCM}(t)$ and $\phi_{S-MCM}(t)$ are the envelope and the instantaneous phase of the pass-band signal considering a sinusoid with frequency equal to f_{SW-RM} as the carrier (see Fig. 9) [5]. Therefore, the RM technique can be used to reproduce MCM schemes. In this case, f_{SW-RM} must be equal to f_{MCM} , $A_V(t)$ must track a reference that is proportional to the envelope of the MCM signal and $\phi_V(t)$ must be equal to its instantaneous phase:

$$A_V(t) = K_{V-S} \cdot A_{S-MCM}(t), \quad (39)$$

$$\phi_V(t) = \phi_{S-MCM}(t). \quad (40)$$

Substituting (39) and (40) into (24) and (25) allows us to obtain how $\alpha(t)$ and $\beta(t)$ must be controlled in order to reproduce MCM schemes:

$$\alpha(t) = \frac{1}{\pi} \cos^{-1} \left(\frac{\pi \cdot K_{V-S} \cdot A_{S-MCM}(t)}{2 \cdot V_G \cdot \sin(d \cdot \pi)} \right). \quad (41)$$

$$\beta(t) = \frac{-\phi_{S-MCM}(t)}{2 \cdot \pi}. \quad (42)$$

III. CONTROL SYSTEM

As previously explained, the RM technique is based on controlling not only the width of the voltage pulses at the input of the filter [i.e., the duty cycle of $v_{S-A}(t)$ and $v_{S-B}(t)$], but also their phase (see Fig. 5). This fact implies that the control system of the power converter is very different from that of power converters based on the PWM technique. Fig. 10 shows the main blocks of the control system. The inputs are the voltage envelope reference [$A_{V-Ref}(t)$], the instantaneous phase reference [$\phi_{V-Ref}(t)$], the measurement of the current that flows through the HB-LEDs [$i_{IO}(t)$] and the DC current reference [$i_{IO-DC-Ref}$], which, in turns, determines the desired lighting level. The outputs are the pulse-voltage waveforms

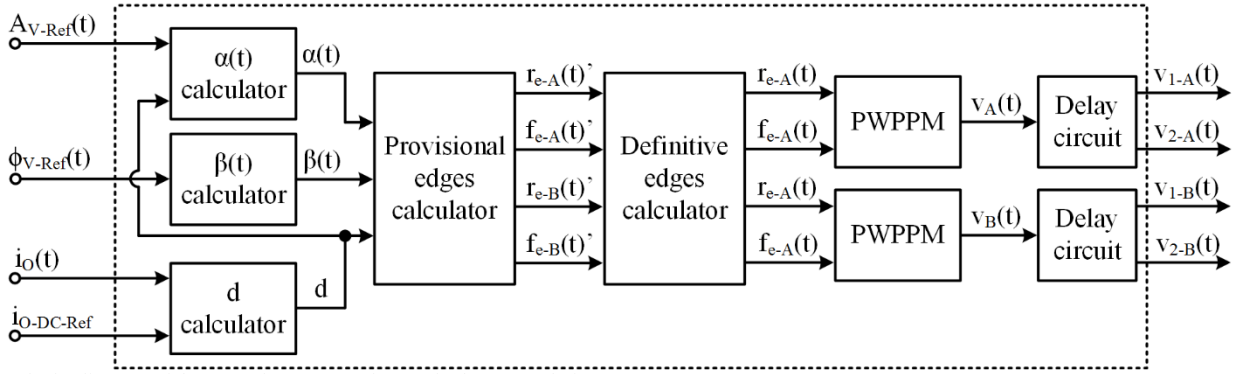


Fig. 10. Blocks diagram of the control system used to apply the RM technique.

that feed the drivers of the MOSFETs: $v_{1-A}(t)$, $v_{2-A}(t)$, $v_{1-B}(t)$ and $v_{2-B}(t)$ for the drivers of Q_{1-A} , Q_{2-A} , Q_{1-B} and Q_{2-B} , respectively. A general description of the main blocks can be found below.

A. Duty Cycle Calculator

This block calculates the d value that is required for properly biasing the HB-LEDs (see Fig. 11). $i_O(t)$ is sensed and filtered to measure i_{O-DC} . Since this value determines the lighting level, $i_{O-DC-Ref}$ is compared to i_{O-DC} . After that, the EA provides the d value that ensures the desired lighting level.

B. $\alpha(t)$ Calculator

As (24) shows, $\alpha(t)$ depends not only on $A_V(t)$, but also on the current d value. As a consequence, both $A_V(t)$ and d feed the $\alpha(t)$ calculator block (see Fig. 10). According to (24), the calculation that must be carried out is not trivial. Fortunately, it can be simplified since, in practice, $A_V(t)$ is much smaller than V_G . This is because of the relationship between the light intensity emitted by an HB-LED, the current that flows through it and the applied voltage: once the HB-LED is properly biased, small voltage changes lead to both big current changes and big light intensity changes [9]. As Fig. 12 shows, the relationship between $\alpha(t)$ and $A_V(t)$ is almost linear when $A_V(t) \ll V_G$ and, as a consequence, the following approximation is quite accurate:

$$\alpha(t) \cong \alpha_0 - m(d) \cdot A_V(t). \quad (43)$$

Note that α_0 is the $\alpha(t)$ value when $A_V(t)$ is 0 V, and m_0 is the derivate of $\alpha(t)$ with respect to $A_V(t)$ and evaluated in $A_V(t)=0$ V:

$$\alpha_0 = 0.5, \quad (44)$$

$$m_0(d) = \frac{1}{2 \cdot V_G \cdot \sin(d \cdot \pi)}. \quad (45)$$

Since m_0 depends on d and the function is not trivial, it is calculated by means of a look-up table (see Fig. 13).

C. $\beta(t)$ Calculator

This block is straightforward because it must multiply $\phi_{V-Ref}(t)$ by $-1/2 \cdot \pi$ according to (25).

D. Provisional Edges Calculator

Once $\alpha(t)$, $\beta(t)$ and d are calculated, the generation of the pulses can be addressed. In the particular case of the two-phase buck converter, both $v_{S-A}(t)$ and $v_{S-B}(t)$ are proportional to the gate signals that feed Q_{1-A} and Q_{1-B} . Obviously, the gate signals that feed Q_{2-A} and Q_{2-B} are the complementary ones. Therefore, and as will be shown below, the remaining part of the control is focused systems on generating the gate signals

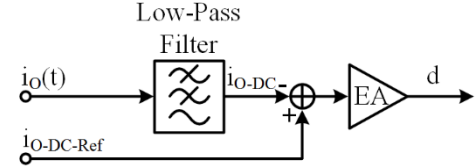


Fig. 11. Duty cycle calculator block.

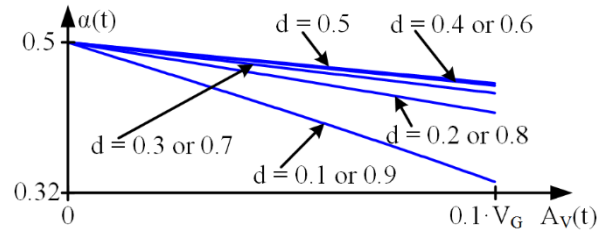


Fig. 12. $\alpha(t)$ versus $A_V(t)$ when $A_V(t) \ll V_G$.

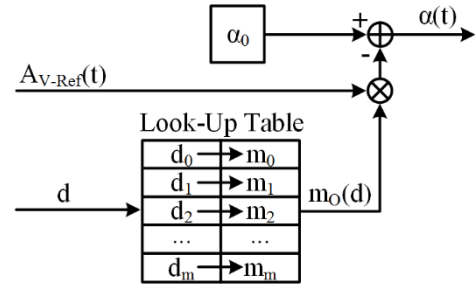


Fig. 13. $\alpha(t)$ calculator block.

that feed Q_{1-A} and Q_{2-A} . The first step is to calculate the provisional rising and falling edges of the pulses. The reason that explains why they are provisional values is included in the next block. According to Section II.B, the provisional edges can be calculated following these equations:

$$r_{e-A}(t)' = \beta(t) - \frac{\alpha(t)}{2} - \frac{d(t)}{2}, \quad (46)$$

$$f_{e-A}(t)' = \beta(t) - \frac{\alpha(t)}{2} + \frac{d(t)}{2}, \quad (47)$$

$$r_{e-B}(t)' = \beta(t) + \frac{\alpha(t)}{2} - \frac{d(t)}{2}, \quad (48)$$

$$f_{e-B}(t)' = \beta(t) + \frac{\alpha(t)}{2} + \frac{d(t)}{2}, \quad (49)$$

where $r_{e-A}(t)'$, $f_{e-A}(t)'$, $r_{e-B}(t)'$ and $f_{e-B}(t)'$ are the provisional rising edge of the phase A, the provisional falling edge of the phase A, the provisional rising edge of the phase B and the provisional falling edge of the phase B, respectively.

E. Definitive Edges Calculator

The aforementioned rising and falling edges are provisional values because they are only valid if the pulse that

must be generated does not exceed the limits of its switching period. Take into account that if a provisional edge is lower than 0 or higher than 1, it means that the pulse exceeds its switching period. Fig. 14 shows an example of a pulse that tries to invade the previous switching period (this situation will be noted as *Situation 2* in the following explanation). Considering the maximum and minimum values of $\alpha(t)$, $\beta(t)$ and d , there are five possible situations:

Situation 1: $r_e(t)' > 0$ and $f_e(t)' < 1$. The pulse does not exceed its switching period. In this situation, the definitive rising and falling edges $[r_e(t)$ and $f_e(t)]$ are equal to the provisional ones.

Situation 2: $r_e(t)' < 0$ and $f_e(t)' < 1$. The pulse exceeds its switching period and a part of it tries to invade the previous one (see Fig. 14). In this situation, the part of the pulse that tries to appear in the previous switching period must be moved to the end of the correct one by adding "1" to $r_e(t)'$.

Situation 3: $r_e(t)' < 1$ and $f_e(t)' > 1$. The pulse exceeds its switching period and a part of it tries to invade the next one. In this situation, the part of the pulse that tries to appear in the next switching period must be moved to the beginning of the correct one by subtracting "1" to $f_e(t)'$.

Situation 4: $r_e(t)' < 0$ and $f_e(t)' < 0$. The pulse exceeds its switching period and tries to invade completely the previous one. In this situation, the pulse must be moved to the correct switching period by adding "1" to both $r_e(t)'$ and $f_e(t)'$.

Situation 5: $r_e(t)' > 1$ and $f_e(t)' > 1$. The pulse exceeds its switching period and tries to invade completely the next one. In this situation, the pulse must be moved to the correct switching period by subtracting "1" to both $r_e(t)'$ and $f_e(t)'$.

F. Pulse-Width and Pulse-Phase Modulator (PWPPM)

This block is responsible for generating the pulses of a buck phase by knowing their definite rising and falling edges. As in the case of PWM, the pulse generation is performed by making a comparison with a sawtooth signal whose frequency is f_{SW-RM} and whose maximum and minimum values are 1 and 0, respectively. Differently from PWM, the sawtooth signal is compared to two references: the definitive rising and falling edges (see Fig. 15). In addition, the output of the PWPPM depends on the aforementioned situations. When the situation is 1, 4 or 5; $f_e(t)$ is higher than $r_e(t)$ and the output is:

$$v(t) = \begin{aligned} &= (\text{Sawtooth} > r_e(t)) \text{ AND } (\text{Sawtooth} < f_e(t)). \end{aligned} \quad (50)$$

On the other hand, when the situation is 2 or 3, $f_e(t)$ is lower than $r_e(t)$ and the output must be:

$$v(t) = \begin{aligned} &= (\text{Sawtooth} > r_e(t)) \text{ OR } (\text{Sawtooth} < f_e(t)). \end{aligned} \quad (51)$$

Fig. 16 shows the scheme of the PWPPM block.

IV. EXPERIMENTAL RESULTS

A two-phase synchronous buck converter with 10th order Butterworth filter ($f_c=650$ kHz) was built in order to validate the proposed technique (see Fig. 17). This power converter supplies a string of five HB-LEDs (W42180 Seoul Semiconductor). The switching frequency is 500 kHz, the input voltage is 30 V and the maximum power is around 10 W. SSM3K361R MOSFETs are used driven by EL7156 ICs. Fig. 18 shows the main waveforms of the HB-LED driver.

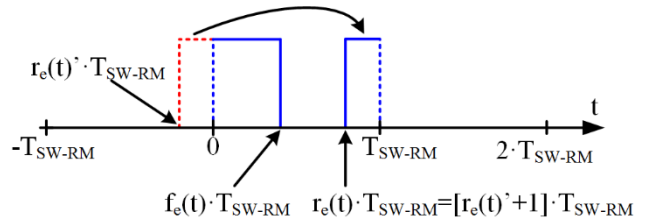


Fig. 14. Example of a pulse that tries to invade the previous switching period.

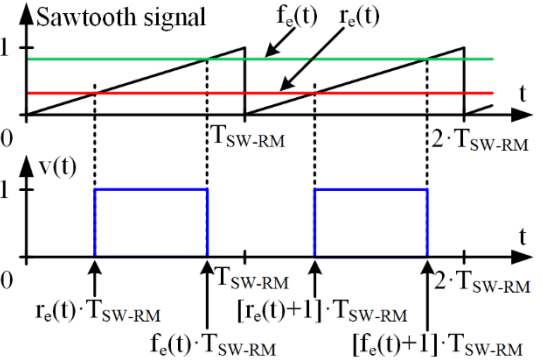


Fig. 15. Main waveforms of the PWPPM block.

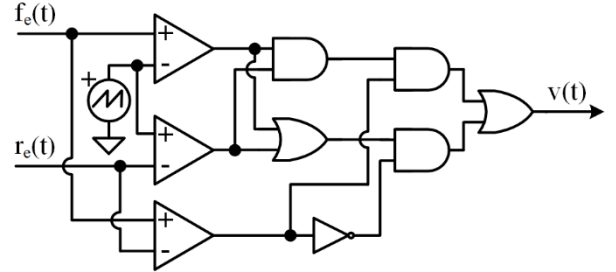


Fig. 16. Scheme of the PWPPM block.

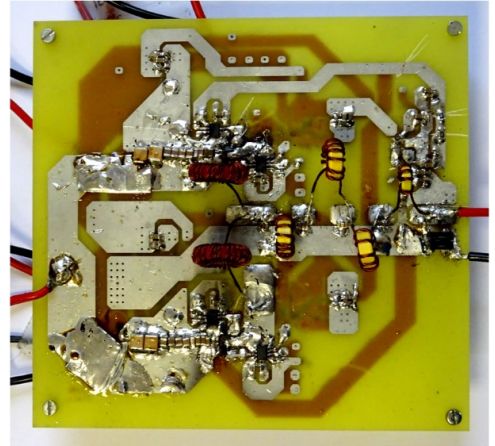


Fig. 17. Prototype of the HB-LED driver for VLC.

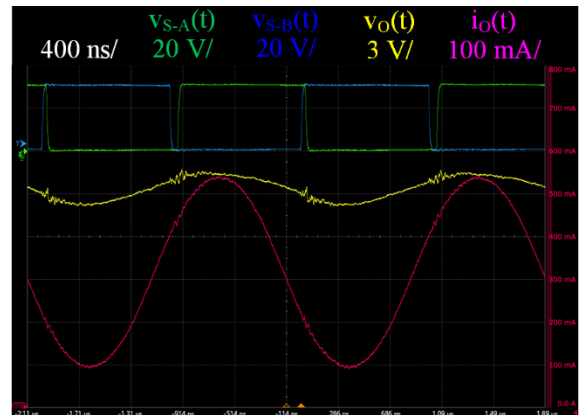


Fig. 18. Main experimental waveforms of the HB-LED driver for VLC.

In order to evaluate the communication capability, a signal that is made up of six carriers is reproduced. Their frequencies are 475 kHz, 485 kHz, 495 kHz, 505 kHz, 515 kHz and 525 kHz. A commercial receiver (PDA10A-EC) provides an output voltage $[V_{RX}(t)]$ proportional to the received light intensity. Fig. 19 shows the main waveforms of the VLC setup when the aforementioned signal is transmitted. The received signal is demodulated offline by using Matlab in order to obtain both the envelope of the received signal $[A_{RX}(t)]$ and its instantaneous phase $[\phi_{RX}(t)]$. This allows us to compare these components to the ideal ones $[A_{Ideal}(t)]$ and $\phi_{Ideal}(t)$, respectively] (see Fig. 20).

Fig. 21 shows that the efficiency of the proposed power converter ranges between 95% and 96.5% depending on the dimming level. It must be pointed out that the efficiency is quite independent of $A_V(t)$.

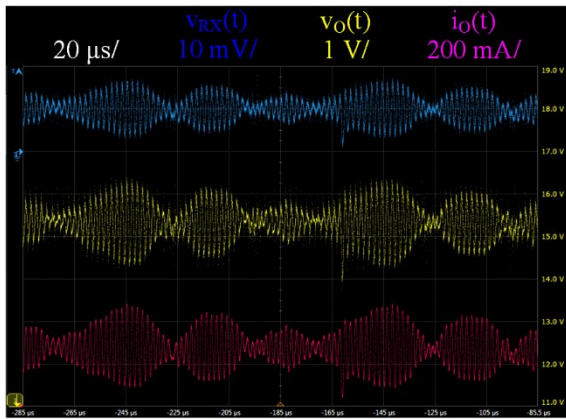


Fig. 19. Main experimental waveforms of the VLC link (distance: 40 cm).

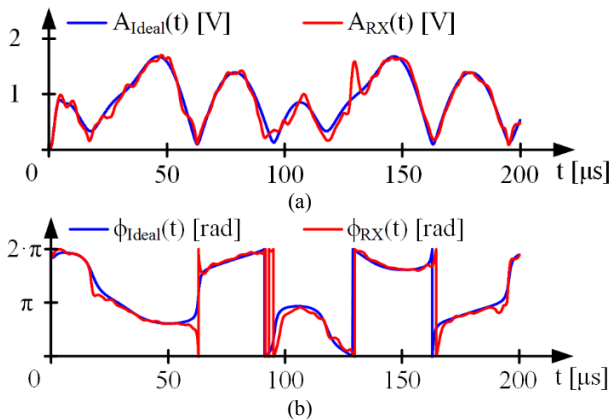


Fig. 20. Comparison between the ideal and the received envelope and instantaneous phase (link distance: 60 cm). Note that the envelopes are normalized to have power equal to 1 W in order to enable the comparison.

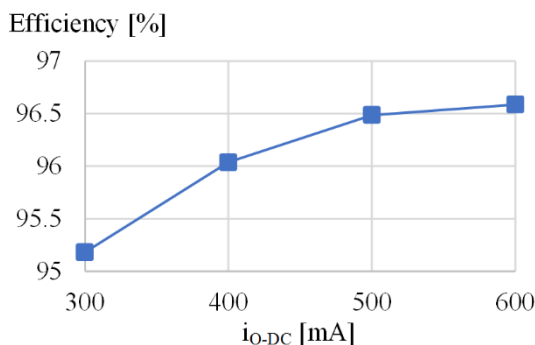


Fig. 21. Efficiency versus dimming level.

V. CONCLUSIONS

The RM technique provides interesting benefits to be applied in the HB-LED drivers of VLC transmitters. Since the power converter is responsible not only for the lighting (i.e., the biasing of the HB-LEDs), but also for the communication, it avoids the use of a LPA. As a consequence, the transmitter is simpler and more efficient. In comparison to other approaches based on the use of pulse-width modulated dc-dc power converters, it requires a much lower switching frequency. In this way, the impact of switching losses is mitigated and both the devices of the power stage and the digital system responsible for the gate signals generation are more affordable. The main drawback of the RM technique is that it has not been demonstrated that it is valid for reproducing MCM schemes, which are the preferred modulation schemes for VLC. This point is successfully addressed in this paper, validating the use of the RM technique for MCM schemes and studying in detail the control system of the power converter.

ACKNOWLEDGMENT

This work has been supported by the Spanish Government under the Project MINECO-17-DPI2016-75760-R, the scholarship FPU14/03268 and the Principality of Asturias under the Project FC-15-GRUPIN14-143, the Project SV-PA17-RIS3-4 and by European Regional Development Fund (ERDF) grants.

REFERENCES

- [1] Cisco Systems, "Cisco visual networking index: global mobile data traffic forecast update, 2016-2021", Feb. 2017. [Online]. Available: <http://www.cisco.com/c/en/us/solutions/collateral/service-provider/visual-networking-index-vni/mobile-white-paper-c11-520862.html>
- [2] A. Jovicic, J. Li and T. Richardson, "Visible light communication: opportunities, challenges and the path to market," in IEEE Communications Magazine, vol. 51, no. 12, pp. 26-32, December 2013.
- [3] A. Wilkins, J. Veitch and B. Lehman, "LED lighting flicker and potential health concerns: IEEE standard PAR1789 update," 2010 IEEE Energy Conversion Congress and Exposition, Atlanta, GA, pp. 171-178, 2010.
- [4] J. Vučić, C. Kottke, K. Habel and K. D. Langer, "803 Mbit/s visible light WDM link based on DMT modulation of a single RGB LED luminary," Optical Fiber Communication Conference and Exposition (OFC/NFOEC), 2011 and the National Fiber Optic Engineers Conference, Los Angeles, CA, 2011, pp. 1-3.
- [5] J. G. Proakis, *Digital communications*, 4th ed. New York: McGrawHill, 2000.
- [6] J. Armstrong, "OFDM for optical communications," in Journal of Lightwave Technology, vol. 27, no. 3, pp. 189-204, Feb. 2009.
- [7] J. Sebastián, D. G. Lamar, D. G. Aller, J. Rodríguez and P. F. Miaja, "On the role of power electronics in visible light communication," in IEEE Journal of Emerging and Selected Topics in Power Electronics.
- [8] J. Rodríguez, D. G. Lamar, D. G. Aller, P. F. Miaja and J. Sebastián, "Efficient visible light communication transmitters based on switching-mode dc-dc converters," in Sensors, vol. 18, no. 4, 2018.
- [9] J. Rodriguez, D. G. Lamar, P. Fernandez Miaja, D. G. Aller and J. Sebastian, "Power efficient VLC transmitter based on pulse-width modulated DC-DC converters and the split of the power," in IEEE Transactions on Power Electronics.
- [10] V. Yousefzadeh, Narisi Wang, Z. Popovic and D. Maksimovic, "A digitally controlled DC/DC converter for an RF power amplifier," in IEEE Transactions on Power Electronics, vol. 21, no. 1, pp. 164-172, Jan. 2006.
- [11] J. Rodriguez, P. Fernandez Miaja, D. G. Lamar and J. Sebastian, "Reproducing single-carrier digital modulation schemes for VLC by controlling the first switching harmonic of the DC-DC power converter output voltage ripple," in IEEE Transactions on Power Electronics.