Optimized design guideline of battery-cell equalizers based on the wave-trap concept

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Abstract—Active equalizers, with a higher efficiency in comparison to passive ones, have the disadvantage of using a considerable number of components. The wave-trap concept allows the battery cell equalizer to use its switching frequency as the control variable that decides which cell is being charged. Hence, the number of controlled switches is strongly reduced without reducing the performance of the system. In this paper, an optimized design guideline for an equalizer based on this concept is presented. First, the mathematical equations that describe the behavior of each trap are introduced. Then, these equations are used for calculating the optimum value of all the parameters of the system. This optimum calculation is based on relating the injected energy in the battery cell and the resonant energy of the chosen trap. This design guideline has been used in the development of a 4-battery-cell equalizer based on a half-bridge structure.

Keywords: battery, cell, equalizer, balancing, wave trap, frequency.

I. INTRODUCTION

Equalizers are needed in order to assure that all the cells connected in series are fully charged even when they have small differences in the capacity. In this way, the energy stored in the battery is maximized without damaging any of the cells. Equalizers can be classified in passive and active equalizers [1], [2]. The first ones are based in dissipative methods, in which the excess of energy is removed from fully-charged cells while the rest of the cells are still being charged [3], [4]. The cost of this kind of equalizers is the lowest one and the control scheme is simple. Nevertheless, the efficiency is considerably low due to the amount of dissipated energy. Moreover, the number of elements is not necessarily low.

Active equalizers, on the other hand, present the highest efficiency as they are based on transferring energy from one element (cell pack or most-charged cell) to another element (most-discharged cell or cell pack) until all the cells reach the fully-charged state. This transferance can be achieved in several ways. One option is using capacitors [4] or inductors [5] for storing the energy extracted from one element (e.g., the most-charged cell or the cell pack) and transferring it to another element (e.g., the most-discharged cell). Another option is using converters which directly transfer the energy between cells and balance them [6]. In all the cases, the number of components is extremely high. Besides, its cost is higher than the cost of passive solutions.

In [7], the wave-trap concept is proposed as a possible option for reducing the number of controlled switches used in an equalizer. The switching frequency (not a complex array of controlled switches) is used for determining which cell is going to receive the extra energy provided by the equalizer. Nevertheless, [7] only proved the validity of the idea, but did not present a detailed design guideline. In this paper, this guideline is developed. It is based on the mathematical equations that described the behavior of a trap and in the ratio resonant energy-transferred energy. Also, components’ tolerances are taken into account.

This paper is organized as follows. A brief description of the proposed concept is provided in section II. A deep insight of the topology is given in section III. The design guideline resulting from the previous sections is explained in section IV. Finally, the experimental results are shown in section V and the conclusions are gathered in section VI.

II. BRIEF DESCRIPTION OF THE PROPOSED CONCEPT

The purpose of this section is providing a brief description of the wave-trap concept proposed in [7] in order to ease the understanding of the following sections.

In Fig. 1a, a string of n traps is presented. Each trap consists of a capacitor and an inductor connected in parallel. Therefore, the impedance of each trap is:

\[ Z_i(\omega) = \frac{j \omega L_i}{1 - \omega^2 L_i C_i}, \quad (1) \]

Fig. 1. a) String of n traps (in grey, a sinusoidal voltage source); b) Impedance of the n traps as a function of the frequency of the sinusoidal voltage applied to the string.
where $Z_i(\omega)$ is the impedance of trap $i$ for the pulsation $\omega$, and $L_i$ and $C_i$ are the inductance and the capacitance of the inductor and the capacitor used in the trap. The resonant frequency $f_i$ of trap $i$ is:

$$f_i = \frac{1}{2\pi \sqrt{L_i C_i}}. \quad (2)$$

Each trap is designed so that its resonant frequency is different from the resonant frequencies of the other traps (Fig. 1b). If this string of traps is supplied with a sinusoidal voltage whose frequency is equal to the resonant frequency of one of the traps, according to (1) its impedance will be considerably higher than the impedance of the other traps (Fig. 1b). Consequently, nearly all the voltage applied to the string will be withstood by this trap. It is possible to take advantage of this concept and use it for a battery equalizer by employing a circuit as the one proposed in Fig. 2a.

### III. Analysis of the Topology

For the sake of simplicity, only the first harmonic component ($V_{HB\_h1}$) of the voltage provided by the half-bridge structure ($V_{HB}$) will be considered [8]-[10].

The Fourier analysis of $V_{HB}$ in Fig. 2a. leads to the following equation:

$$v_{HB\_h1}(t) = A_{h1}(D, V_{in}) \sin(2\pi f_{sw} t + \Phi_{sw}), \quad (3)$$

where:

$$A_{h1}(D, V_{in}) = \frac{2V_{in}}{\pi} \sin(\pi D) \quad \text{and} \quad \Phi_{sw} = \pi \left(1 - \frac{2D}{2}\right), \quad (4)$$

$D$ is the duty cycle of MOSFET $M_1$, and $V_{in}$ is the input voltage of the half-bridge structure.

The half-bridge structure will always operate with a switching frequency $f_{sw}$ equal to the resonant frequency $f_i$ of one of the traps. Therefore, the voltage withstanded by the chosen trap $V_{Trap\_i}(t)$ can be approximated by $V_{HB\_h1}(t)$:

$$v_{Trap\_i}(t) \approx v_{HB\_h1}(t) = A_{h1}(D, V_{in}) \sin(2\pi f_{i} t + \Phi_i), \quad (5)$$

Equations (4) and (5) show that the amplitude of $v_{Trap\_i}(t)$ can be controlled by means of the duty cycle $D$.

The chosen trap shown in Fig. 2b has been redrawn in Fig. 3a. In this figure, the capacitor $C_i$ has been replaced with the voltage source $v_{Trap\_i}(t)$ due to the high quality factor of the filter that each trap represents. The behavior of the trap will change depending on whether the diode is reverse biased or not. When it is reverse biased, the valid circuit is the one presented in Fig. 3b. When it is directly biased the trap will behave as the circuit presented in Fig. 3c. These two circuits it is possible to calculate the electric charge that the trap transfers to the cell in each switching period.

For the sake of clarity, in the next explanation $v_{Trap\_i}(t)$ will be referred to $t'$, a different time reference. Equation (6) can be rewritten as:

$$v_{Trap\_i}(t') = A_{h1}(D, V_{in}) \sin(2\pi f_i t') \quad \text{where} \quad t' = t + \frac{\Phi_i}{2\pi f_i}. \quad (7)$$

When the diode is not directly biased (Fig. 3b), the current through the magnetizing inductance is equal to the current through the leakage inductance. This current can be calculated using the sinusoidal steady-state phasor analysis because the diode conduction time is much shorter than the resonant period. Therefore:

![Fig. 3. a) Equivalent circuit of the trap; b) when the diode is reverse biased; c) when the diode is directly biased](image-url)
\[ i_{lm,i}(t') = i_{lk,i}(t') = \frac{V_{trap,i}(t')}{2\pi f_i L_i} = \frac{1}{2\pi f_i L_i} A_{bi}(D, V_{in}) \sin \left(2\pi f_i t' - \frac{\pi}{2} \right), \]

where:

\[ L_i = L_{lk,i} + L_{m,i}. \]

In the same way, the current through the capacitor of the trap is:

\[ i_{c,i}(t') = j2\pi f_i C_i V_{trap,i}(t') = \frac{2\pi f_i C_i A_{bi}(D, V_{in})}{2} \sin \left(2\pi f_i t' + \frac{\pi}{2} \right). \]

Both currents have been represented in Fig. 4a.

The diode will be directly biased at \( t'_{ch ini} \):

\[ t'_{ch ini} = \frac{1}{2\pi f_i} \left( V_{cell,i} + V_{knee} \right) \left( \frac{L_i}{r_{n,i}} + \frac{\pi}{2} V_i \sin (\pi D) \right). \]

When the diode is directly biased, the valid circuit is Fig. 3c. Therefore, the current injected into the chosen cell is:

\[ i_{cell,i}(t') = \frac{1}{r_{n,i}} (i_{lk,i}(t') - i_{lm,i}(t')). \]

Taking into account that \( i_{lm,i}(t'_{ch ini}) = i_{lk,i}(t'_{ch ini}) \), (13) becomes:

\[ i_{cell,i}(t') = \frac{A_{bi}(D, V_{in})}{2\pi f_i} \left( \frac{V_{cell,i} + V_{knee}}{r_{n,i}^2} \right) \left( t'_{ch ini} - t' \right) \left( \frac{1}{L_{lk,i}} + \frac{1}{L_{m,i}} \right). \]

The diode will be directly biased until the current injected into the cell becomes zero, at \( t'_{ch end} \):

\[ i_{cell,i}(t'_{ch end}) = 0. \]

Finally, the average current injected into the chosen battery cell can be calculated very easily:

\[ I_{cell,i} = \frac{1}{t'_{ch end} - t'_{ch ini}} \int_{t'_{ch ini}}^{t'_{ch end}} i_{cell,i}(t') dt'. \]

The current injected into the cell should be controlled in order to be able to precisely define the final voltage of the cell. As can be seen in (14), the current injected into the cell depends on the amplitude of the first harmonic component of the voltage withstood by the trap \( A_{bi}(D, V_{in}) \) and, therefore, on the duty cycle \( D \). As a consequence, the proposed equalizer can control the amount of energy injected into the cell during each switching period.

The maximum value of the current \( i_{cell,i}(t') \), given in equation (14), corresponds to the maximum value of \( A_{bi}(D, V_{in}) \), which takes place when \( D=0.5 \). Therefore,

\[ i_{cell,i \_ max}(t') = V_i \left( \cos \left(2\pi f_i t'_{ch ini} \right) - \cos \left(2\pi f_i t' \right) \right) + \frac{r_{n,i} L_{lk,i}}{r_{n,i}^2} f_i \]

Considering this equation and (16), the maximum average current injected into the cell can be defined as:

\[ I_{cell,i \_ max} = \frac{1}{t'_{ch end} - t'_{ch ini}} \int_{t'_{ch ini}}^{t'_{ch end}} I_{cell,i \_ max}(t') dt'. \]

Defining \( L_{p,i} \) as the parallel connection of \( L_{lk,i} \) and \( L_{m,i} \), and \( \lambda_i \) as:

![Fig. 4. a) Main voltages and currents; b) detail of the currents between \( t'_{ch ini} \) and \( t'_{ch end} \).](image-url)
\[
\lambda_i = \frac{L_{ik,i}}{L_{n,i}} = \frac{L_{m,i} + L_{ik,i}}{L_{m,i}},
\]

(19)
equation (17) becomes:
\[
i_{cell,i,max}(t') = \frac{1}{t_{r,i}^2} \frac{V_{in}}{2 \pi f_i L_{ik,i}} \left[ 2 \pi f_i V_{tr} \left( \cos(2 \pi f_i t_{ch.ini}) - \cos(2 \pi f_i t') \right) \right],
\]

(20)
and solving:
\[
\Delta \phi_i = \phi_{in} - \phi_{end}.
\]

(30)

The normalized maximum average current injected into the cell is:
\[
\Gamma_i = \frac{i_{cell,i,max}}{i_{base,i}} = \frac{1}{2 \pi} \int_{0}^{\pi} \gamma_i(\phi, d\phi).
\]

(31)
It should be taken into account that the magnetizing inductance of the transformer is considerably higher than its leakage inductance. Therefore, (19) becomes:
\[
\lambda_i \approx 1,
\]

(32)
and hence, (12), (27), (28) and (29) become:
\[
t_{ch.ini} \approx \frac{1}{2 \pi f_i} a \sin \left( \frac{V_{cell,i} + V_{knee,i}}{2 \sin(\pi D)} \right),
\]

(33)
\[
\gamma_i \approx \frac{1}{r_{tr,i}} \left[ 2 \pi f_i \left( \cos \phi_{in} - \cos \phi_{end} \right) + \mu_i(\phi_{in} - \phi_{end}) \right],
\]

(34)
\[
\phi_{in} \approx a \sin \left( \frac{\mu_i \pi}{r_{tr,i}} \right),
\]

(35)
\[
\cos \phi_{in} - \cos \phi_{end} \approx \frac{\mu_i \pi}{r_{tr,i}} (\phi_{end} - \phi_{in}).
\]

(36)

For the sake of simplicity, it can be considered that the voltage of all the cells is nearly the same (i.e., \( V_{cell,i} = V_{cell} \)) because the voltage imbalance is usually small in comparison to the voltage of the cells. Also, the input voltage of the half-bridge structure is the voltage of the cell pack. Therefore, \( V_{in} = V_{cell} n_{cell} \), \( n_{cell} \) being the number of cells connected in series. As a consequence, equation (21) becomes:
\[
\mu_i = \frac{1 + \nu_{knee}}{n_{cell}},
\]

(37)
where \( \nu_{knee} \) is:
\[
\nu_{knee} = \frac{V_{knee}}{V_{cell}}.
\]

(38)
The evolution of the conduction angle \( \Delta \phi \) as a function of the turns ratio \( r_{tr,i} \) for different number of cells \( (n_{cell}) \) and for different values of \( \nu_{knee} \) can be computed from (30), (35), the solution of (36), and (37). Fig. 5 shows the results obtained for values of \( n_{cell} \) around 4, which will be used in section IV.B and section V.

In the same way, \( \Gamma_i \) can be also expressed as a function of \( r_{tr,i}, n_{cell}, \) and \( \nu_{knee} \) The results are given in Fig. 6.

IV. DESIGN GUIDELINE
In the design of the proposed trap-based equalizer it is mandatory to take into account several issues.

A. Influence of tolerances
For a given trap, the nominal resonant frequency is defined by (2). For this analysis, it has been assumed that the resonant frequency of trap \( i \) is lower than the resonant frequency of trap \( i+1 \):
The actual resonant frequency of any trap will be inside a range defined by the tolerances of its inductor and its capacitor. Its resonant frequency may be as low as:

\[ f_{\text{min},i} = \frac{1}{2\pi \sqrt{L_i (1 + \text{tol}_L) C_i (1 + \text{tol}_C)}} \]

where \( \text{tol}_L \) and \( \text{tol}_C \) are the tolerances of the inductor and the capacitor of the trap. Its maximum resonant frequency due to tolerances is:

\[ f_{\text{max},i} = \frac{1}{2\pi \sqrt{L_i (1 - \text{tol}_L) C_i (1 - \text{tol}_C)}} \]

\[ = \frac{f_i}{\sqrt{(1 - \text{tol}_L)(1 - \text{tol}_C)}} \]

The proposed equalizer would work properly even when the resonant frequencies of the traps were not exactly equal to the theoretical ones as long as their relative positions were not affected. If this condition is introduced in the design process, then the resulting system is robust and is not negatively affected by tolerances. The mathematical expression of this condition is:

\[ f_{\text{max},i} < f_{\text{min},i+1} \]

\[ \Rightarrow f_i > \frac{f_{\text{min},i+1}}{\sqrt{(1 - \text{tol}_L)(1 - \text{tol}_C)}} \]

\[ = \frac{f_{\text{min},i+1}}{\sqrt{(1 + \text{tol}_L)(1 + \text{tol}_C)}} \]

Therefore:

\[ f_{i+1} > \frac{(1 + \text{tol}_L)(1 + \text{tol}_C)}{\sqrt{(1 - \text{tol}_L)(1 - \text{tol}_C)}} \quad \text{where } \tau_{\text{tol}} \text{ is defined by the tolerances of the trap’s components.} \]

The design should start by choosing the minimum value of \( f_i \):

\[ f_{\text{min},i} = \frac{1}{2\pi \sqrt{L_i C_i (1 + \text{tol}_L)(1 + \text{tol}_C)}} \]

so,

\[ L_i C_i = \frac{1}{\left(2\pi f_{\text{min},i}\right)^2 \left(1 + \text{tol}_L\right) \left(1 + \text{tol}_C\right)} \]

Once the product \( L_i C_i \) of the first trap is obtained, it is possible to follow an iterative process in which (43) is used to calculate the \( L \cdot C \) product of the next trap based on the \( L \cdot C \) product of the previous one.

**B. Calculation of the inductance, the capacitance and the turns ratio of each trap**

There are infinite solutions for designing each trap as only the corresponding \( L_i C_i \) value has been obtained in section IV.A. Nevertheless, it is possible to optimize the design of the proposed equalizer considering additional conditions which will lead to specific values of the capacitance and the inductance of each trap.

The specific impedance \( Z_i \) of each trap can be defined as:

\[ Z_i = \frac{f_i}{\sqrt{L_i C_i}} \]

The resonant current driven by the inductor of the chosen trap was defined in (9). Considering this equation, (2) and (46):

\[ i_{\text{in},i}(t) = i_{\text{in}},(t') = \frac{\Delta i_{\text{in}}(D, V_a)}{Z_i} \sin\left(2\pi f_i t' - \frac{\pi}{2}\right) \]
In the same way, the current driven by the capacitor (defined in (11)) is:
\[ i_{c_i}(t') = \frac{A_{in}(D,V_{in})}{Z_i} \sin \left( 2\pi f_i t' + \frac{\pi}{2} \right). \]  
\[ (48) \]

Considering \( D=0.5 \), (47) and (48) becomes:
\[ i_{lm_{-i_{-\text{max}}}}(t') = i_{ik_{-i_{-\text{max}}}}(t') = \frac{2V_{in}}{Z_i} \sin \left( 2\pi f_i t' + \frac{\pi}{2} \right), \]
\[ (49) \]
\[ i_{c_{i_{-\text{max}}}}(t') = \frac{2V_{in}}{Z_i} \sin \left( 2\pi f_i t' + \frac{\pi}{2} \right). \]
\[ (50) \]

The ratio between the energy in the resonant circuit (i.e., chosen trap) and the energy injected into the chosen cell during each switching period is denoted by \( Q_{E_{-i}} \) and can be defined as:
\[ Q_{E_{-i}} = \frac{E_{res_{-i}}}{E_{cell_{-i}}}, \]
\[ (51) \]
where \( E_{res_{-i}} \) is the energy in the resonant circuit and \( E_{cell_{-i}} \) the energy injected into the cell. If the amount of energy injected into the cell is lower than the amount of resonant energy that the chosen trap is handling, then the behavior of the string of traps will not depend on whether the diode is reverse biased or not. This means that the voltage across the capacitor of the trap will be sinusoidal even during the short periods of time when the diode is conducting. Hence, it is interesting to design the system with values of \( Q_{E_{-i}} \) higher than unity so that the behavior of the trap is not affected by the cell (see later in this section).

The resonant energy in the chosen trap can be obtained from its voltage and the value of its capacitor (when the voltage in the trap is the peak one, the energy in the inductor is zero):
\[ E_{res_{-i}} = C_i \frac{2V_{in}^2}{\pi^2}. \]
\[ (52) \]

The energy injected into the cell in each switching period can be denoted as:
\[ E_{cell_{-i}} = \frac{I_{cell_{-i_{-\text{max}}}}V_{cell_{-i}}}{f_i}. \]
\[ (53) \]

Hence,
\[ Q_{E_{-i}} = \frac{2f_i C_i V_{in}^2}{\pi^3 V_{cell_{-i}} I_{cell_{-i_{-\text{max}}}}}. \]
\[ (54) \]

Taking into account (2) and (46), then (54) becomes:
\[ Q_{E_{-i}} = \frac{V_{in}^2}{\pi^3 Z_i V_{cell_{-i}} I_{cell_{-i_{-\text{max}}}}}. \]
\[ (55) \]

Fig. 7 shows the waveforms corresponding to the voltage across the resonant capacitors and the current injected into the cells obtained for different values of \( Q_{E_{-i}} \). As Fig. 7 shows, the voltage across the capacitor can be considered sinusoidal when the value of \( Q_{E_{-i}} \) is higher than two. However, very high values of \( Q_{E_{-i}} \) lead to high circulating currents through the resonant capacitor and inductor, as can be deduced from (49), (50) and (55). As a consequence, a tradeoff must be met when choosing the values of \( Q_{E_{-i}} \) in order to obtain a system in which efficiency is not excessively affected while the voltage across the resonant capacitor can be considered sinusoidal even when the diode is directly biased. Values of \( Q_{E_{-i}} \) around 2 (or slightly higher) can be considered good options. In fact, \( Q_{E_{-i}} \) can be designed to be lower than two (as in Fig. 7a or Fig. 7b).

Nevertheless, in such a case, the voltage is excessively distorted and the behavior of the system cannot be predicted by the proposed equations.

With (19), (31), (37) and (38), equation (55) can be rewritten as:
\[ Q_{E_{-i}} = \frac{1}{\pi^2} \frac{1}{\lambda_i} \frac{1}{\left( \mu_i - V_{knee} \right) f_i}, \]
\[ (56) \]
which will be used in the design guideline.

The calculation of the precise values of \( L_i \) and \( C_i \) should be made as follows. First of all, the value of \( V_{knee} \) can be calculated using (38). With \( V_{knee} \) and the number of cells, \( n_{cell} \), the value of \( \mu_i \) can be then obtained from (37). Choosing a reasonable value for the conduction angle \( \Delta \phi \), (i.e., around \( 30^\circ \)), and with \( \mu_i \), it is possible to obtain the value of the turns ratio of the transformers, \( r_{tr_{-i}} \), by means of equations (30), (35) and (36). In Fig. 5, equation (30) is graphically represented: the conduction angle is depicted as a function of \( r_{tr_{-i}} \) for different values of \( n_{cell} \) and \( V_{knee} \) (both define the value of \( \mu_i \)). Equation (35), along with \( r_{tr_{-i}} \) and \( \mu_i \), can be used to obtain the value of \( \phi_{end} \) and, consequently, the value of \( \phi_{end} \) according to the chosen value of \( \Delta \phi \). \( f_i \) can be calculated using \( \phi_{end} \), \( \lambda_{end} \), \( \phi_{tr_{-i}} \), \( \mu_i \) and equations (31) and (34). This has been graphically represented in Fig. 6. As has been explained, the value of \( Q_{E_{-i}} \) should be high enough in order to assure the proper operation of the resonant traps (as can be seen in Fig. 7, any value of \( Q_{E_{-i}} \) higher than 2 will lead to
satisfactory results). Therefore, once $\Gamma_i$ is obtained, equation (56) can be used to determine the minimum value of $\lambda_i$ for the chosen value of $Q_{E_i}$. This minimum value of $\lambda_i$ (see equation (19)), should be lower than the value of $\lambda_i$ presented by any of the transformers of the traps once they are built. $I_{\text{cell max}}$, the maximum average equalizing current, is a parameter defined by the designer. Therefore, if the value of $\lambda_i$ is satisfactory (i.e., low enough), then equation (55) can be used to obtain the value of $Z_i$ according to the chosen value of $Q_{E_i}$. With $Z_i$ (equation (46)) and $f_i$ (its calculation was presented in section IV.A), it is possible to obtain the required values of $L_i$ and $C_i$.

C. Design of the magnetics

The switching frequency of the proposed system is variable but, ideally, each inductor is going to withstand appreciable voltage only at a frequency equal to the resonant frequency of the trap it belongs to. When the switching frequency of the system is different from the resonant frequency of a trap, the voltage across that trap is almost zero (i.e., low enough), then equation (55) can be used to obtain the value of $Z_i$ according to the chosen value of $Q_{E_i}$. When $Z_i$ (equation (46)) and $f_i$ (its calculation was presented in section IV.A), it is possible to obtain the required values of $L_i$ and $C_i$.

V. EXPERIMENTAL RESULTS

A prototype has been built following the design guideline and with the purpose of validating it. It has been designed for a pack of four cells connected in series. The switching frequency range is 100 kHz-215 kHz considering tolerances of 7.5% in both, inductors and capacitors (see Table 1). The value of each resonant frequency $f_i$ has been obtained according to the design process described in section IV.A. The value of $\lambda_{\text{max}}$ is around 0.2; therefore, $\mu_i$ is 0.3. As can be seen in Fig. 5, for a conduction angle of around 30°, the required turns ratio is 0.48. It may advisable to increase this value so that even due to tolerances, the RDSON of the MOSFETs, the influence of other traps, etc. the required voltage in the secondary side of the transformer is 40 V. After 92 minutes, the voltage of the four cells is close to their nominal value (around 3.3 V). After 92 minutes, the voltage of each cell is close to their nominal value.

In Fig. 6, it can be observed that the proposed equalizer is working properly. The voltage of the secondary side of the transformer is presented for three different duty cycles. As can be seen, the proposed system can determine the amplitude of the voltage withstand by the chosen trap by means of the duty cycle. This implies that the charging process (final voltage of the cell, equalization current, etc.) can be perfectly controlled.

In Fig. 7, the secondary-side voltage of the transformer implemented in trap 3 ($V_{\text{sec},3}$), the voltage of cell 3 ($V_{\text{cell},3}$), the equalization current of this cell during a charging period ($I_{\text{cell},3}$) and the voltage applied to trap 3 ($V_{\text{trap},3}$) referred to the secondary side) are presented for three different duty cycles. As can be seen, the charging process is as described in section III. It should be noted that the diode is operating at zero-current switching.

In Fig. 8, $V_{\text{sec}}$, the voltage withstood by string of traps, and $V_{\text{trap},3}$, the voltage withstood by the chosen trap (in this case, trap 3), are shown for three different values of $D$ but for the same value of switching frequency (166 kHz in order to choose trap 3). As can be seen, the proposed system can determine the amplitude of the voltage withstood by the chosen trap by means of the duty cycle. This implies that the charging process (final voltage of the cell, equalization current, etc.) can be perfectly controlled.

In Fig. 9, the secondary-side voltage of the transformer implemented in trap 3 ($V_{\text{sec},3}$), the voltage of cell 3 ($V_{\text{cell},3}$), the equalization current of this cell during a charging period ($I_{\text{cell},3}$) and the voltage applied to trap 3 ($V_{\text{trap},3}$) referred to the secondary side) are presented for three different duty cycles. As can be seen, the charging process is as described in section III. It should be noted that the diode is operating at zero-current switching.

In Fig. 10a, Fig. 10b and Fig. 10c, the proposed equalizer is connected to real 2.5-Ah lithium-ion battery cells. In Fig. 10a, the operation of the proposed equalizer is presented when the voltage of each cell is close to their nominal value (around 3.3 V). After 92 minutes, the voltage of the four cells is within the desired range and the half-bridge converter stops its operation (i.e., both MOSFETs are turned-off). After 150 minutes from the start of the equalization process, the voltage of the four cells is stabilized and the final imbalance is around 5 mV. It should be taken into account that using a simple estimation of the OCV of the cells as the criterion for defining which one has to be charged is not an optimum approach. More precise methods can be found in literature [11]-[13]. Nevertheless, the implementation of an optimum Battery Management System (BMS) is out of the scope of this paper.

In Fig. 10b a similar test is carried out, but in this case one of the cells has reached 3.6 V (i.e., the recommended charging voltage) while cell 2, 3 and 4 have a lower voltage (unbalanced operation of the battery charger). As in the previous case, the cell with the lowest voltage is considered under charged. The final imbalance, after the stabilization
time (around 50 minutes), is close to 10 mV.

Finally, in Fig. 10c, a detail of the charging process of one of the cells is presented (the stabilization time is not shown).

VI. CONCLUSIONS

In this paper, an optimized design guideline for equalizers based on the wave-trap concept has been presented.

The ratio between the resonant energy in the chosen trap and the energy injected into the corresponding cell is the key parameter. This ratio should be high enough to assure that the resonant energy in the trap is not affected by the injection of part of that energy into the cell. Also, this value should be low enough so that resonant energy does not lead to too-high losses and low efficiency. This ratio should have a value around unity in order to obtain a trade-off.

Also, tolerances in the components are important. Traps have to be located in such a way that variations in the switching frequency due to tolerances do not lead to the overlap of two adjacent traps.

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